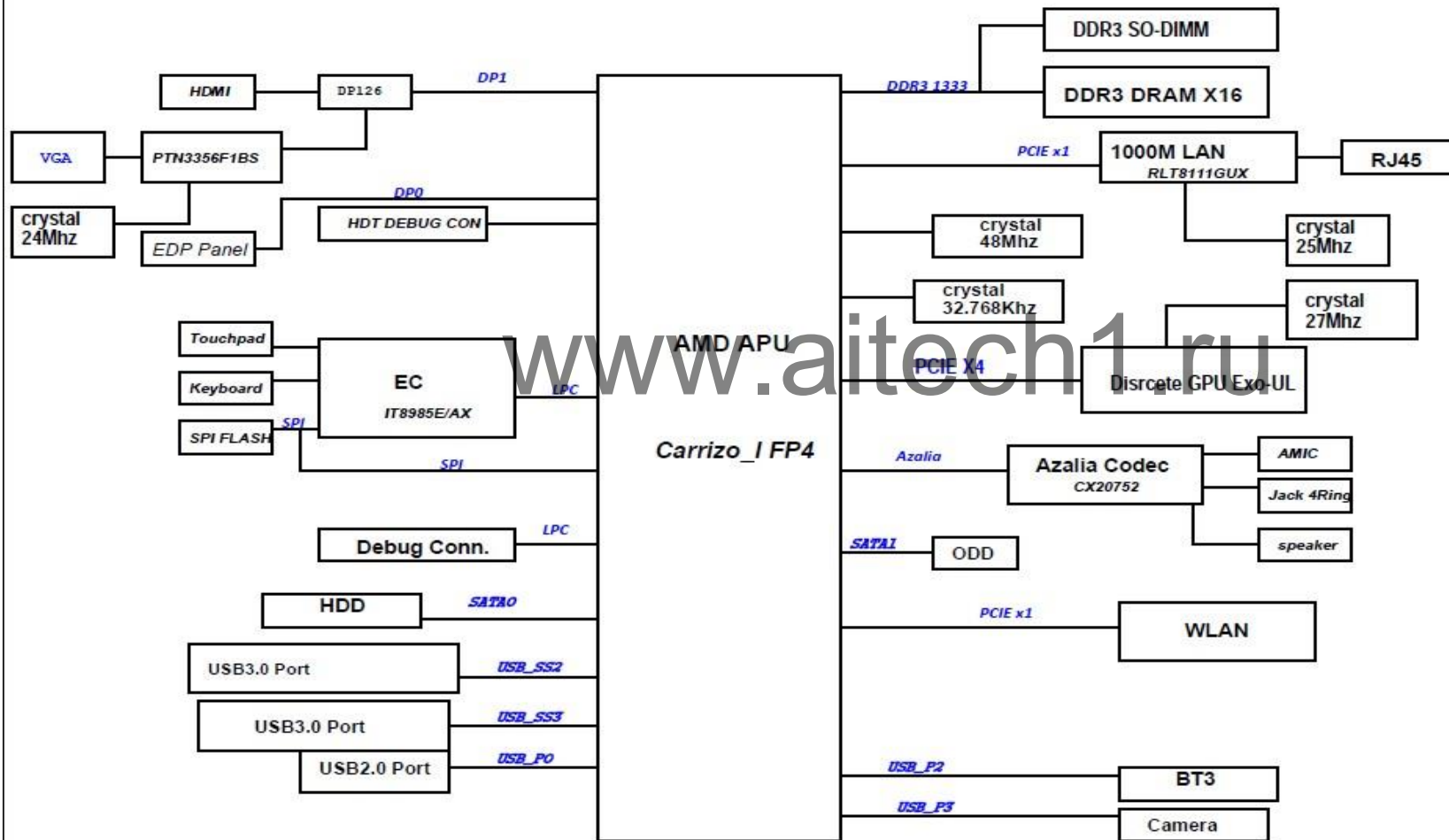
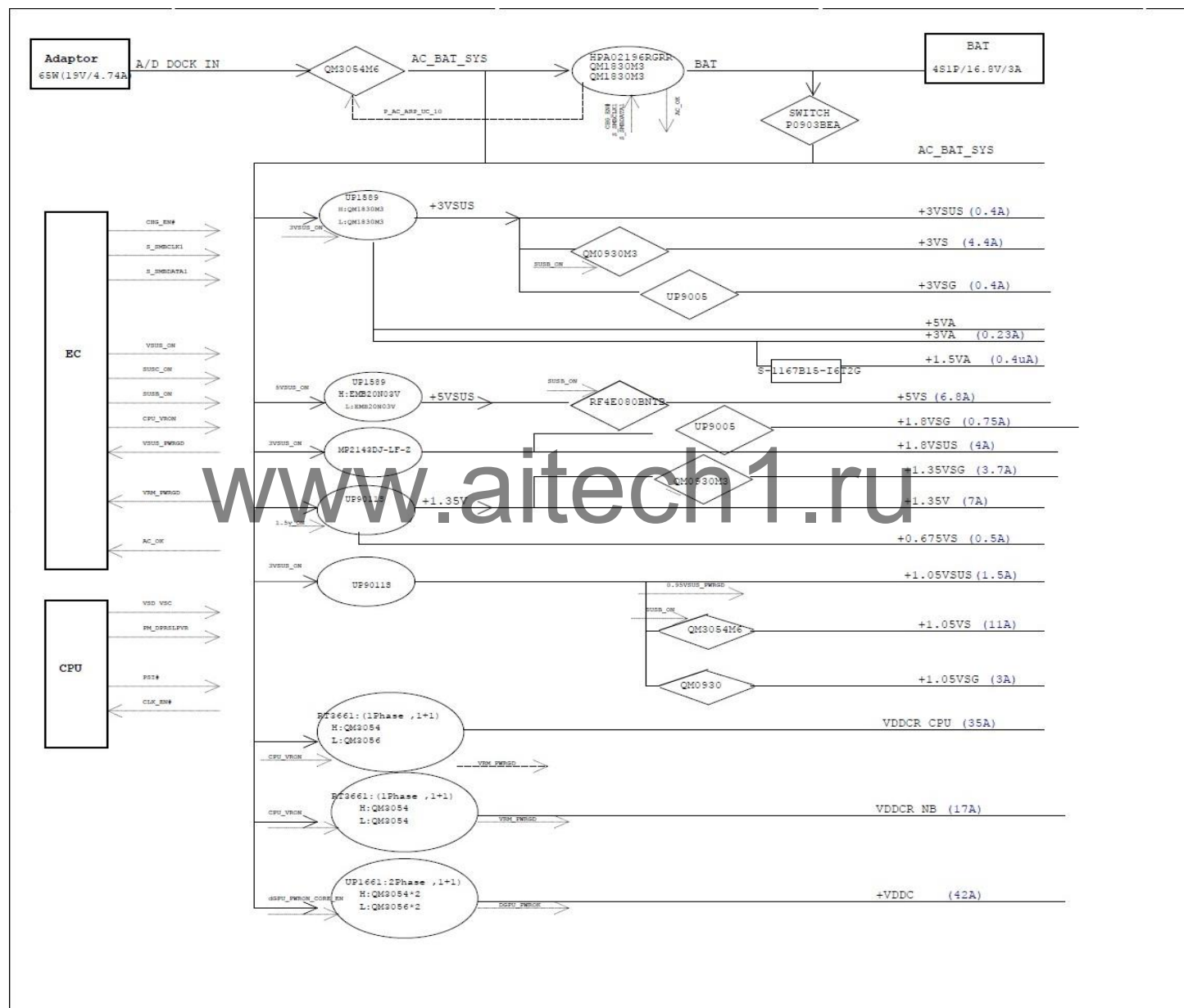


BLOCK DIAGRAM

X751YI Schematic R2.0



POWER FLOW

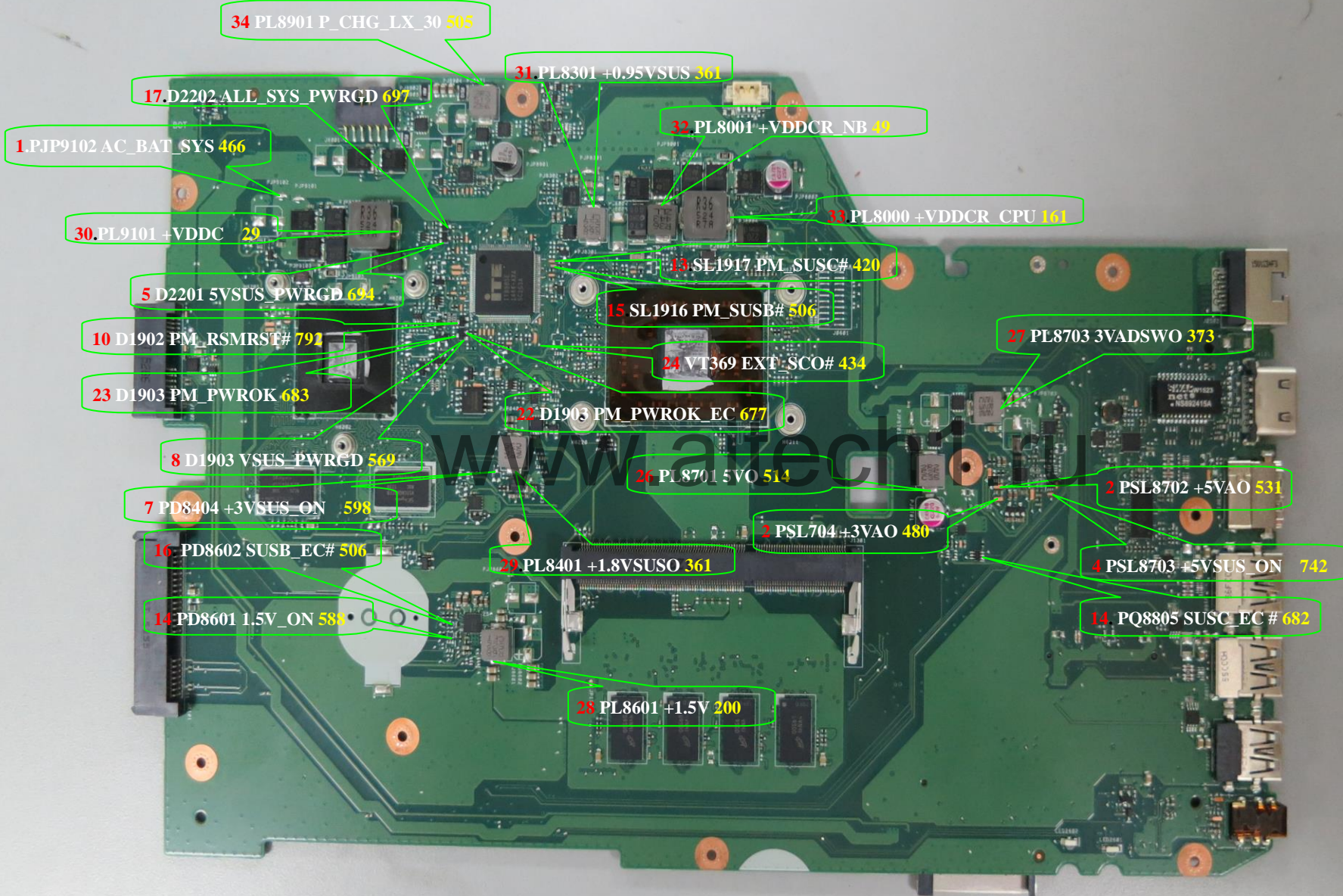


The diagram illustrates the power-up sequence for an AMD Carrizo FT4 APU system. Key components and signals include:

- Power Sources:** AC_BAT_SYS, +3VA, +5VA, +3VA EC, +5VSUS, +1.5V, +1.5VS, +0.95VS, +1.8VS, +3VS, +5VS, VDDCR_CPU, VDDCR_NB, LVDS PANEL.
- EC (IT8985) and APU (GA20) Interactions:**
 - EC outputs: VSUS_PWRGD (6), 5VSUS_PWRGD (3), SUSC_EC# (12), SUSB_EC# (14), CPU_VRON (16), APU_PWRGD (17), EXT_SCI# (22), PWR_GOOD (21).
 - APU outputs: SLP_S5# (11), SLP_S3# (13), A20GATE (18), RC_IN# (19), KBRST# (20), LPC_RST# (23), PM_PWRON (24).
- EC CODEC (WLAN, LAN) and PWR_LED#:**
 - EC CODEC outputs: PWR_LED# (9), PWR_LED# (10), PWR_LED# (11), PWR_LED# (12), PWR_LED# (13), PWR_LED# (14), PWR_LED# (15), PWR_LED# (16), PWR_LED# (17), PWR_LED# (18), PWR_LED# (19), PWR_LED# (20), PWR_LED# (21), PWR_LED# (22), PWR_LED# (23), PWR_LED# (24).
 - PWR_LED# (9) is connected to PWR_LED# (10) and PWR_LED# (11).
 - PWR_LED# (10) is connected to PWR_LED# (11) and PWR_LED# (12).
 - PWR_LED# (11) is connected to PWR_LED# (12) and PWR_LED# (13).
 - PWR_LED# (12) is connected to PWR_LED# (13) and PWR_LED# (14).
 - PWR_LED# (13) is connected to PWR_LED# (14) and PWR_LED# (15).
 - PWR_LED# (14) is connected to PWR_LED# (15) and PWR_LED# (16).
 - PWR_LED# (15) is connected to PWR_LED# (16) and PWR_LED# (17).
 - PWR_LED# (16) is connected to PWR_LED# (17) and PWR_LED# (18).
 - PWR_LED# (17) is connected to PWR_LED# (18) and PWR_LED# (19).
 - PWR_LED# (18) is connected to PWR_LED# (19) and PWR_LED# (20).
 - PWR_LED# (19) is connected to PWR_LED# (20) and PWR_LED# (21).
 - PWR_LED# (20) is connected to PWR_LED# (21) and PWR_LED# (22).
 - PWR_LED# (21) is connected to PWR_LED# (22) and PWR_LED# (23).
 - PWR_LED# (22) is connected to PWR_LED# (23) and PWR_LED# (24).

The sequence of events is numbered 1 through 24, starting from the power-on switch (4) and ending with the system ready (24). A timing diagram on the right shows the relationship between EC_RSMRST#, PM_PWRBTN#, and PWR_LED#.

Signal Measure Point-Bottom



Signal Measure Point-Top

